

JIT MODAK

(Assistant Professor)

M.Tech in Microelectronics & VLSI Technology

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Objective

Seeking a challenging and needful work environment where I can utilize my knowledge, consultancy and extract my inherent skill as a professional, to take up greater responsibilities and contribute my best to the organization as well as add value to my career. Also looking for an association with people-oriented organizations where individual talent, honesty, hard work culture, intelligence can be reckoned.

Employment History

Organization	Location	From	To	Role
Regent Education & Research Foundation	Barrackpore	August ,2024	Till date	Assistant Professor
Regent Education & Research Foundation	Barrackpore	August ,2019	July,2024	Technical Assistant
CMC Limited	Kolkata	September,2013	January,2014	Technical Analyst

Skills

- Advance Micro & Nano Device, Microelectronics Technology, Analog & Digital Electronics, Microprocessor & Microcontroller, Algorithms, Control System, Signals & System, Analog Communication, Power Electronics.
- Work on SILVACO TCAD, Xilinx, Cadence, Multisim, MATLAB, EMU 8086, MS Office.
- Platform/OS: Windows 7, Windows 10, Linux.

Academic Record

Qualification	Marks Obtained	University/Board	Year of passing
M.Tech (Microelectronics & VLSI Technology)	8.10 (CGPA)	Maulana Abul Kalam Azad University of Technology	2024
B.Tech. (ECE)	8.38 (CGPA)	West Bengal University of Technology	2013
Higher Secondary (H.S.)	70 %	West Bengal Council of Higher Secondary Education	2009
Madhyamik (M.P.)	72.25 %	West Bengal Board of Secondary Education	2007

Trainings & FDP

- PHP training from NIIT from June, 2016 to March, 2017
- Core Java training from Annapurna Comptech on from January, 2013 to June, 2013
- Advance Telecom from BSNL on July, 2012
- Tele maintenance from South Eastern Railway on June, 2012
- Transmission system and technique from Prasar Bharati on December, 2011
- FDP on Writing Securing Grants for Technical Institutions, 27th to 29th June, 2023
- FDP on Dreamweaving: Turning Ideas into creative realities , 6th to 10th Feb, 2024
- FDP on Recent Trends In Renewable Energy Sector, 13th to 17th May, 2025
- FDP on Quantum Intelligence: Integrating Quantum Technology with AI and ML, 18th to 23rd Aug, 2025
- FDP on GREEN HORIZON, 15th to 20th Sep, 2025

Project

Course	Name of Project	Duration
M.Tech	Design of a junctionless gate-all-around (GAA) FinFET using Silvaco & its application in Biosensor	1 year

B.Tech	Water Activated Alarm	1 year
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Extra-Curricular Activities/Achievements

- Member of Sports committee, NSS committee in current organization.
- Member of Library committee.
- Mentorship role and Student database maintenance in current organization.
- PGET Qualified (2022)

Personal Details

Father's Name: Jagai Chandra Modak
DOB : 16-08-1991

Languages known: English (Expert), Hindi (Speak) and Bengali (Proficient)
Marital Status : Unmarried

Declaration

I hereby declare that the information given above is true to the best of my knowledge.

Date: 25.09.25
Place: Halisahar

Jit Modak

Signature